

## Tuesday, November 27<sup>th</sup>, 2018

Time	Titles	Speakers or additional info
08:30-08:50	Registration	
08:50-09:00	Welcome / Introduction	Knut Båtstoløkken
09:00-10:00	Key Note Session	Chairman: Knut Båtstoløkken
09:00-10:00	"Trends in the use of robotics in the electronic production and test of devices"	Søren Peter Johansen, Danish Technological Institute, Denmark
10:00-10:30	Exhibitor Forum: short presentations	Chairman: Mick Austin
10:30-11:00	Coffee Break / Exhibition	
11:00-12:30	Session 1: Test Strategies 1	Chairman: Stig-Gunnar Jensen
11:00-11:30	"In-line PCBA testing"	Henrik Frost, Danfoss Peder D. Lomborg, DSE
11:30-12:00	"PCB Design for Test and Implementation of Test Strategies"	Mark Laing, Mentor, A Siemens Business
12:00-12:30	"Defect free-products: what is the best DfT flow in order to deliver defect-free PCBA"	Christophe Lotz, Aster
12:30-13:30	Lunch	
13:30-15:00	Session 2: Structural Test	Chairman: Mattias Ericsson
13:30-14:00	"How to test Power Electronics?"	Lothar Diez, Spea
14:00-14:30	"Increasing ICT coverage with Powered Framescan and protect your devices with Safe Test"	Peter Kuhlemann, Teradyne
14:30-15:00	"The Next Generation of Manufacturing Board Test: Testing the technologies of the future"	Jun Balangue, Keysight Technologies
15:00-15:30	Coffee Break / Exhibition	
15:30-16:30	Session 3: Cobots in Test Automation	Chairman: Magnus Rönqvist
15:30-16:00	"The Automation challenge - The Usage of Collaborative Robots in Testing and In-Circuit Tests "	Thomas Goetz Keysight Technologies
16:00-16:30	"Flexible automation supported using Cobots (Collaborative robots)"	Per Kloster Poulsen, Universal Robots
16:30-17:00	News from conferences	Chairman: Artur Jutman
17:00-17:30	Fruit & Refreshments / Exhibition	
17:30-19:00	Panel debate: "Robotics in testing vs traditional automation?"	Panel moderator: Birger Schneider
19:30	Dinner	



## Wednesday, November 28<sup>th</sup>, 2018

Time	Titles	Speakers or additional info
09:00-10:30	Session 4: Test Strategies 2	Chairman: Paulius Musteikis
09:00-09:30	"To Software Platform or Not to Software Platform"	Mattias Ericsson, AddQ
09:30-10:00	" Embedded Functional Test and Measurement"	Artur Jutman, Testonica Lab
10:00-10:30	"A Soft Approach to Manufacturing Execution Systems"	Vidar Grønås, Virinco
10:30-11:00	Coffee Break / Exhibition	
11:00-12:30	Session 5: Reliability, RF & Other Challenges	Chairman: Birger Schneider
11:00-11:30	"Reliable product development based on physics of failure"	Suzanne Otto, FORCE Technology
11:30-12:00	Reliable Electronics? Precise Current Measurements May Tell You Otherwise	Hans Manhave, Ridgetop
12:00-12:30	"Production PCBa RF testing @ Cobham Satcom"	Torben Krogsgaard, Cobham Satcom
12:30-13:30	Lunch	
13:30-15:00	Session 6: JTAG Based Test & Interconnects	Chairman: Lars Kongsted-Jensen
13:30-14:00	"Synthetic Instruments for design validation and production test"	Jan Heiber, GÖPEL Electronics
14:00-14:30	"Concurrent IJTAG a next Step in IJTAG-based Test"	Rene Krenz-Baath, Hochschule Hamm-Lippstadt
14:30-15:00	"New Innovative and Modular Mass Interconnect Solution with Electromechanical Locking ODU-MAC® Black-Line"	Markus Rochau, ODU GmbH & Co KG
15:00-15:10	Closing Session: TestForum concluding remarks	Knut Båtstoløkken Kitron
15:10-15:20	Break	
15:20-18:25	Workshop on HI2OT: "Nordic University Hub on Industrial Internet of Things" See details on next page for the IoT workshop	Chairman: Erik Larsson
	Wine & cheese served at a convenient time during the IoT workshop	



## Wednesday, November 28<sup>th</sup>, 2018 IoT Workshop

## **Detailed program for IoT Workshop**

Time	Titles	Speakers or additional info
15:20-18:25	Workshop on HI2OT: "Nordic University Hub on Industrial Internet of Things"	Chairman: Erik Larsson
15:20-15:30	Opening	Erik Larsson, Lund University
15:30-15:55	"Secure Industrial IoT"	Christian Gehrmann, Lund University
15:55-16:20	"Safe and secure wireless communication for cooperative systems"	Stig Petersen, SINTEF, Norway
16:20-16:45	"Reliability in Resource-Constrained Wireless Networks with IEEE 802.15.4 TSCH"	Xenofon Fafoutis, Danish Technichal University, Denmark
16:45-17:05	Wine & cheese break	
17:05-17:30	"Fault-tolerant topology and routing synthesis for IEEE time-sensitive networking"	Bahram Zarrin, Danish Technichal University, Denmark
17:30-17:55	"Fault management; chip to system"	Erik Larsson, Lund University
17:55-18:20	Panel: How to securing the supply-chains at PCB manufacturing? Panelists: Christian Gehrmann, Lund University Stig Petersen, Sintef, Norway Xenofon Fafoutis, DTU, Denmark Bahram Zarrin, DTU, Denmark	Moderator: Erik Larsson
18:20-18:25	Closing and wrap-up	Erik Larsson, Lund University