

EQUIPTEST

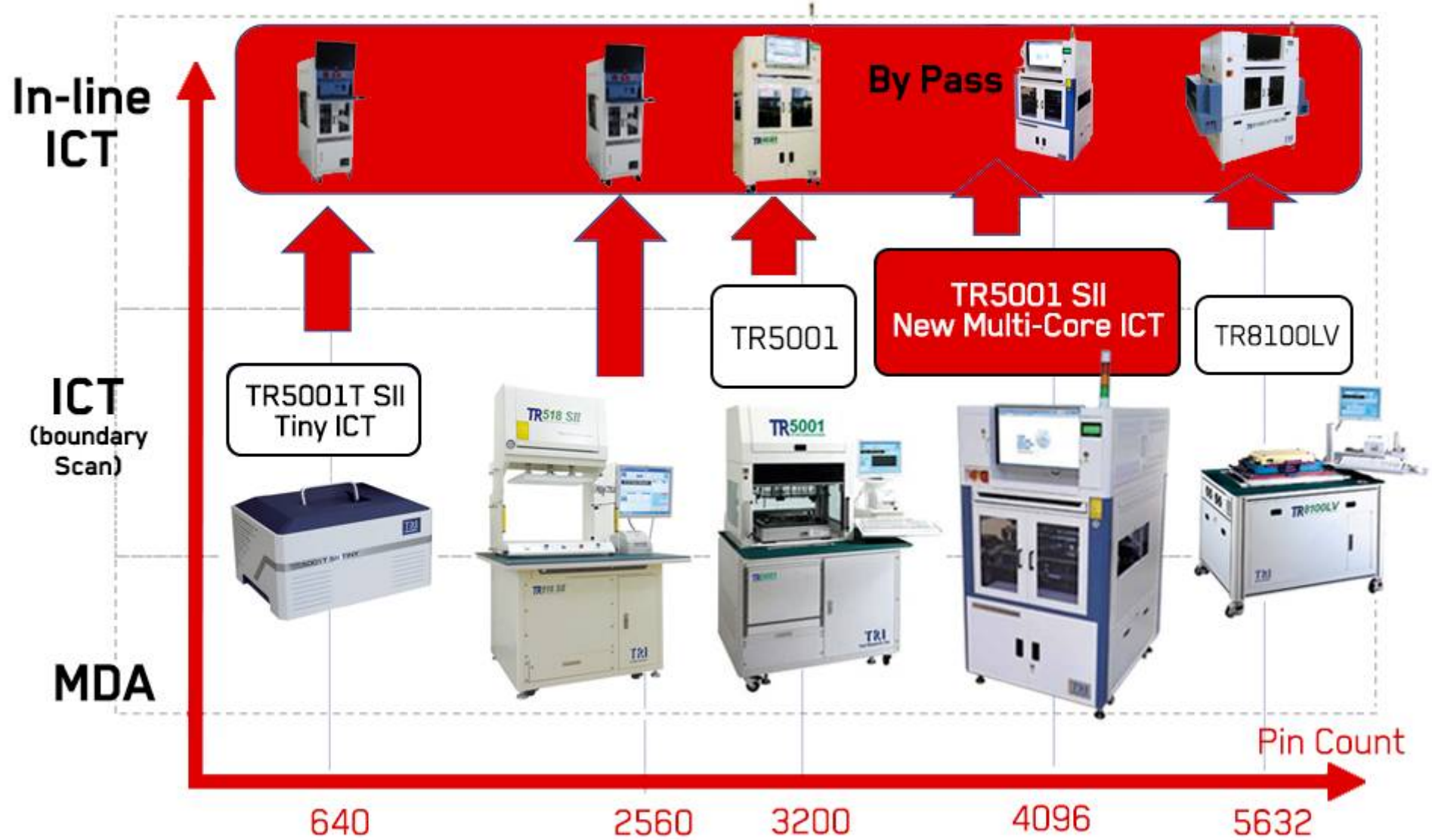


TRI TESTSYSTEMS



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TR518 SII TR518L SII	TR5001T SII	TR5001 TR5001L	TR5001D SII TR5001Q SII
2560 Pts 3456 Pts	640 Pts	3200 Pts 4224 Pts	3328 Hybrid Pts 4096 Hybrid Pts
High-End MDA	Compact ICT	ICT+MDA	Parallel/Merge Core ICT + FCT
R/L/C Measure TestJet Measure Hi-V current source GPIO:auto-Stamp	On-Power Volt/Freq Measure BSCAN Link Instrument	On-Power Volt/Freq Measure BSCAN Pin Driver Link Instrument	On-Power Volt/Freq Measure BSCAN2 Pin Driver/Receiver Per Pin Link Instrument
PCBA Manufacturing Test	Flex PCB Lo-TPs DUT	White goods Automotive electrical	Hi-Accuracy DUT Multi-board DUTs



**TINY SII INLINE
TR518 SII INLINE**



TR5001 INLINE



TR8100LVP INLINE



**TR5001Q SII
INLINE**

Max. Test Points	640 (ICT) 1024 (MDA)	2496 (QDI) +704 (Manual)	2496 (QDI) +704 (Manual)	4096(QDI)
Device Class	Compact Inline ICT/MDA	Inline ICT+MDA	High End Inline ICT+MDA	High End Parallel Inline ICT+MDA
Conveyor	Optional Bypass	Optional Bypass	Standard Bypass	Optional Bypass
PCB Specs	Min: 50(W) x 50(L) mm	Min: 70(W) x 70(L) mm	Min: 70(W) x 70(L) mm	Min: 70(W) x 70(L) mm
	Max: 250(W) x 330(L) mm	Max: 300(W) x 450(L) mm	Max: 280(W) x 360(L) mm	Max: 300(W) x 450(L) mm
	Thickness: 0.6~5 mm	Thickness: 0.6~5 mm	Thickness: 0.6~5 mm	Thickness: 0.6~5 mm
Max. Component Height	Top: 30mm Bottom: 25mm	Top: 90mm Bottom: 30mm	Top: 90mm Bottom: 30mm	Top: 90mm Bottom: 30mm
Fixture Connection	Manual	QDI	QDI	QDI
Application	Lo-TPs DUTs	White goods Automotive electrical	Hi-TPs DUT Network	Hi-Accuracy DUT Multi-board DUTs

TRI Testsystem –TR5001 SII



[TR5001 SII Parallel ICT](#)



TR5001 SII INLINE



TR5001 SII VACUUM



TR5001 SII MANUAL HANDLER



System

- Dual/Quad-core support parallel test
- Merged multi-core to increase pin count
- High accuracy capacitance measurement
- Fully automated calibration

New Modules

- High density 128Pin HSWB
- Serial Test Controller support JTAG/SPI/UART

New Software

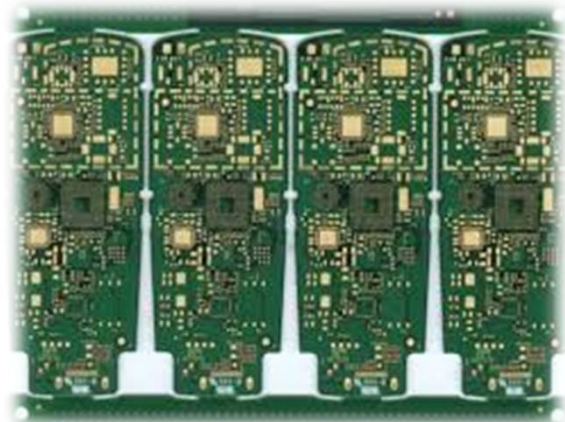
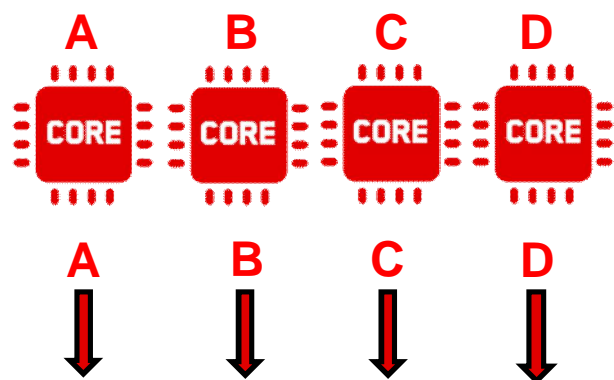
- Brand new user interface
- Generate Test program based on CAD data
- Open platform for functional test

Fixture Connections

- Fixture types: Press, Vacuum, DIN-QDI, Drawer, same fixtures in-line and off-line
- Automation Inline solution
- Hands-free Safe operation

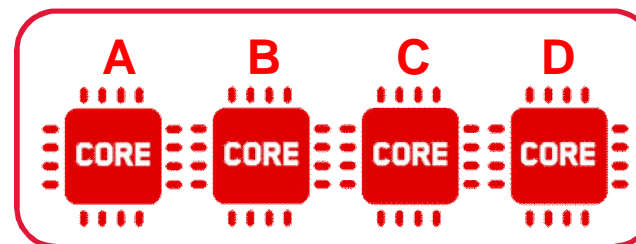


Independent Multi-Core



Multi-Core parallel test
for panelized DUT

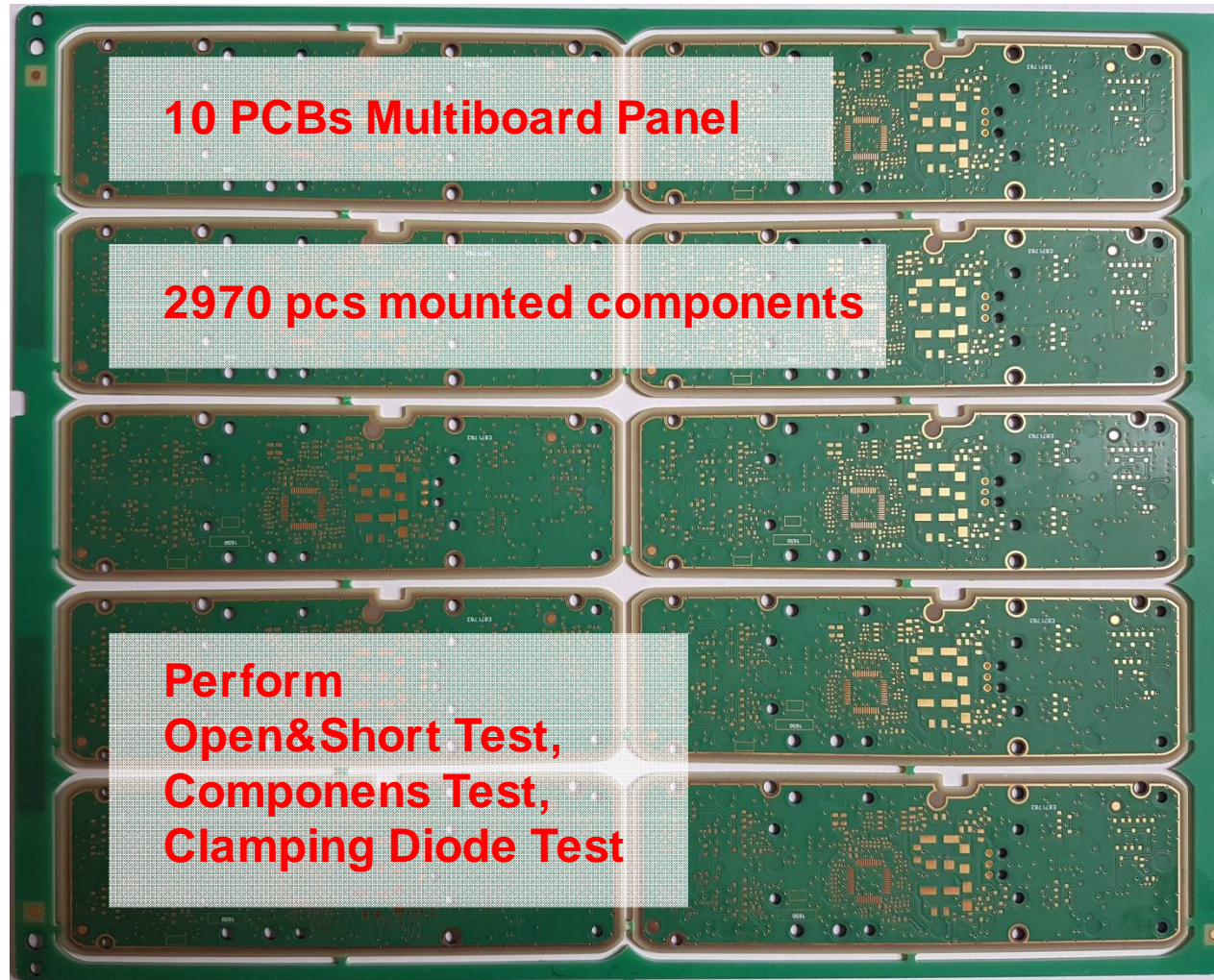
Merged Multi-Core



A+B+C+D

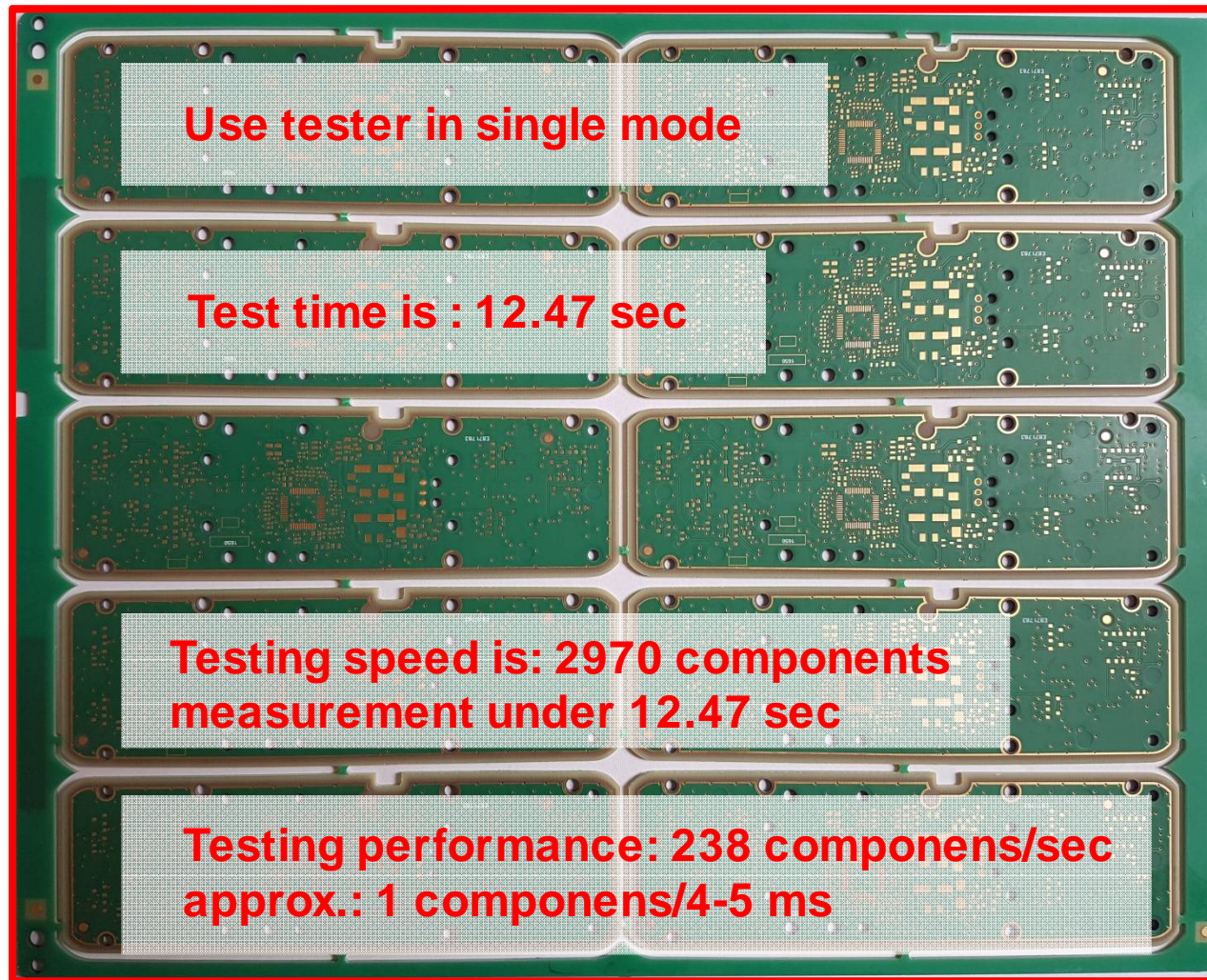


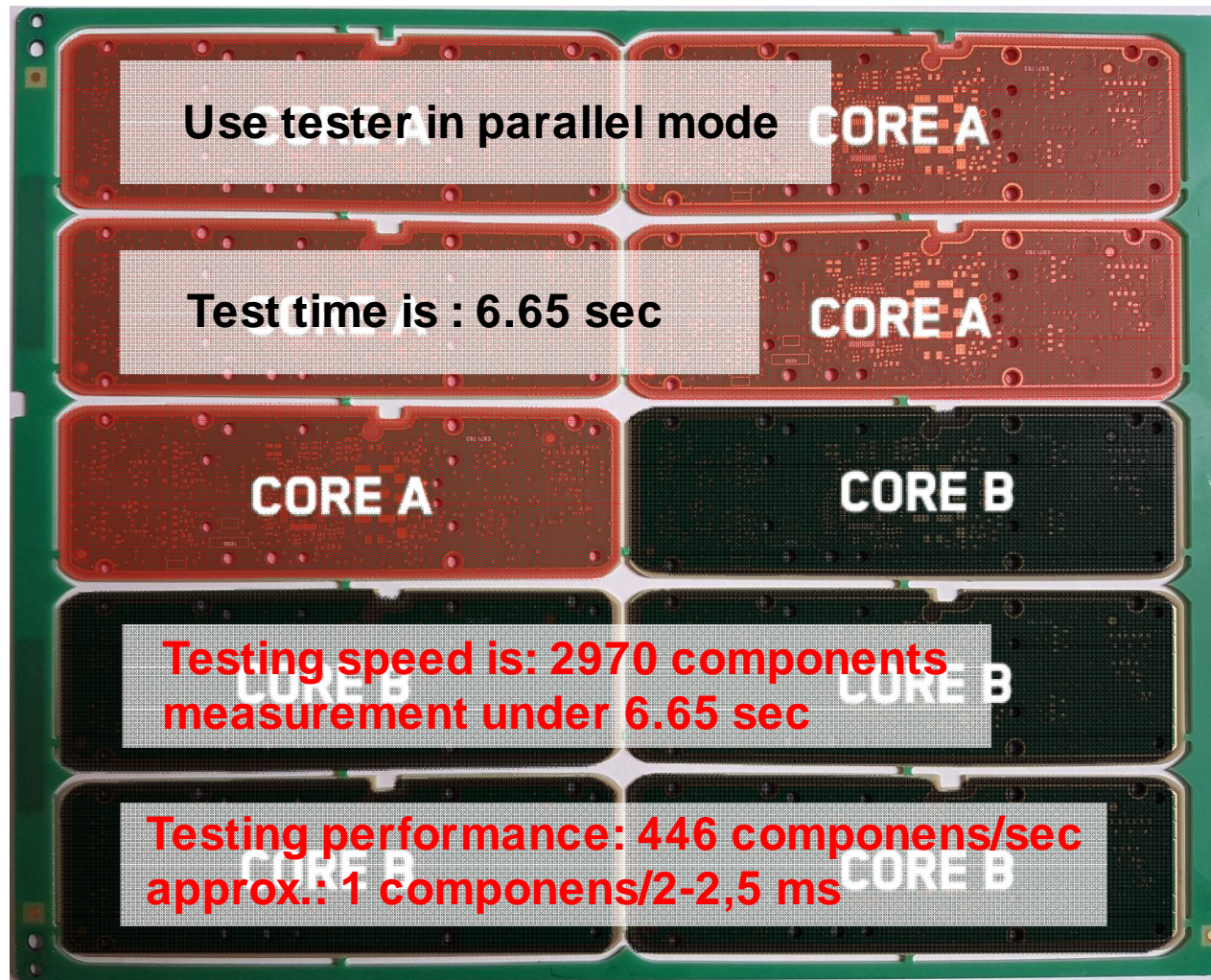
Merge Multi-Core to test
high pin count DUT

**Project: 10 PCBs MultiBoard ICT TEST**



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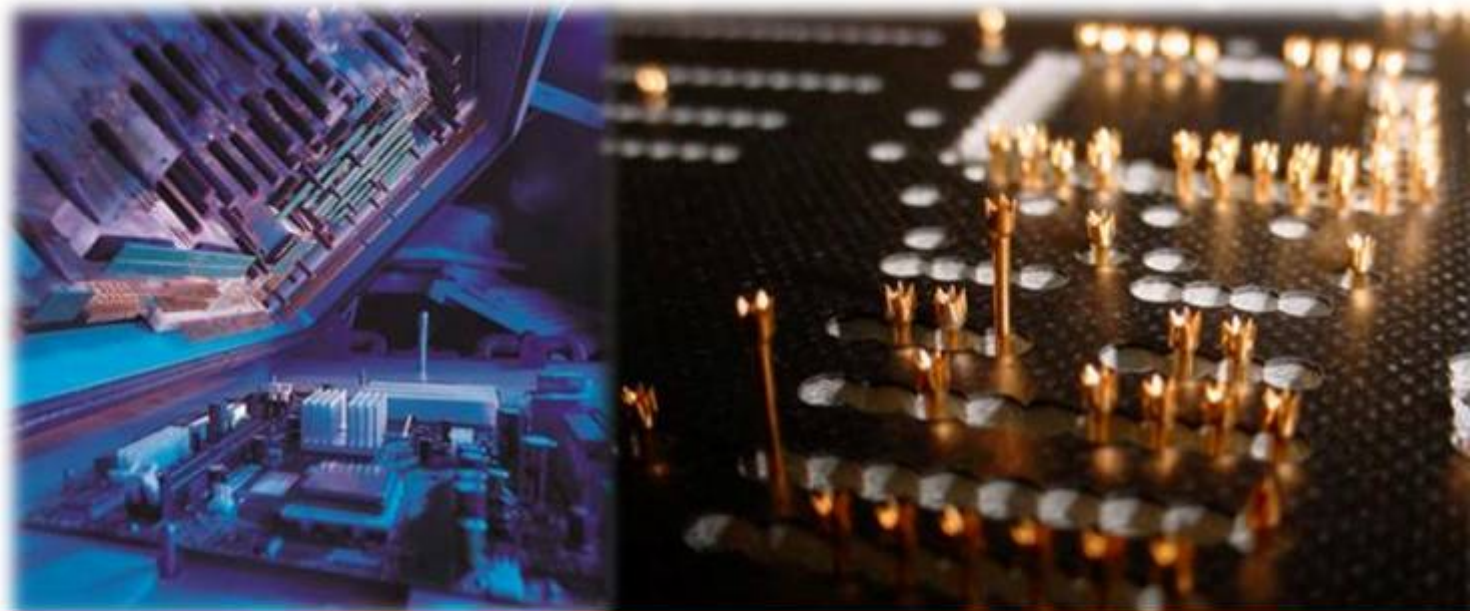


**Project: 10 PCBs MultiBoard ICT TEST**

TRI Testsystem – TR5001 SII





EQUIPTEST



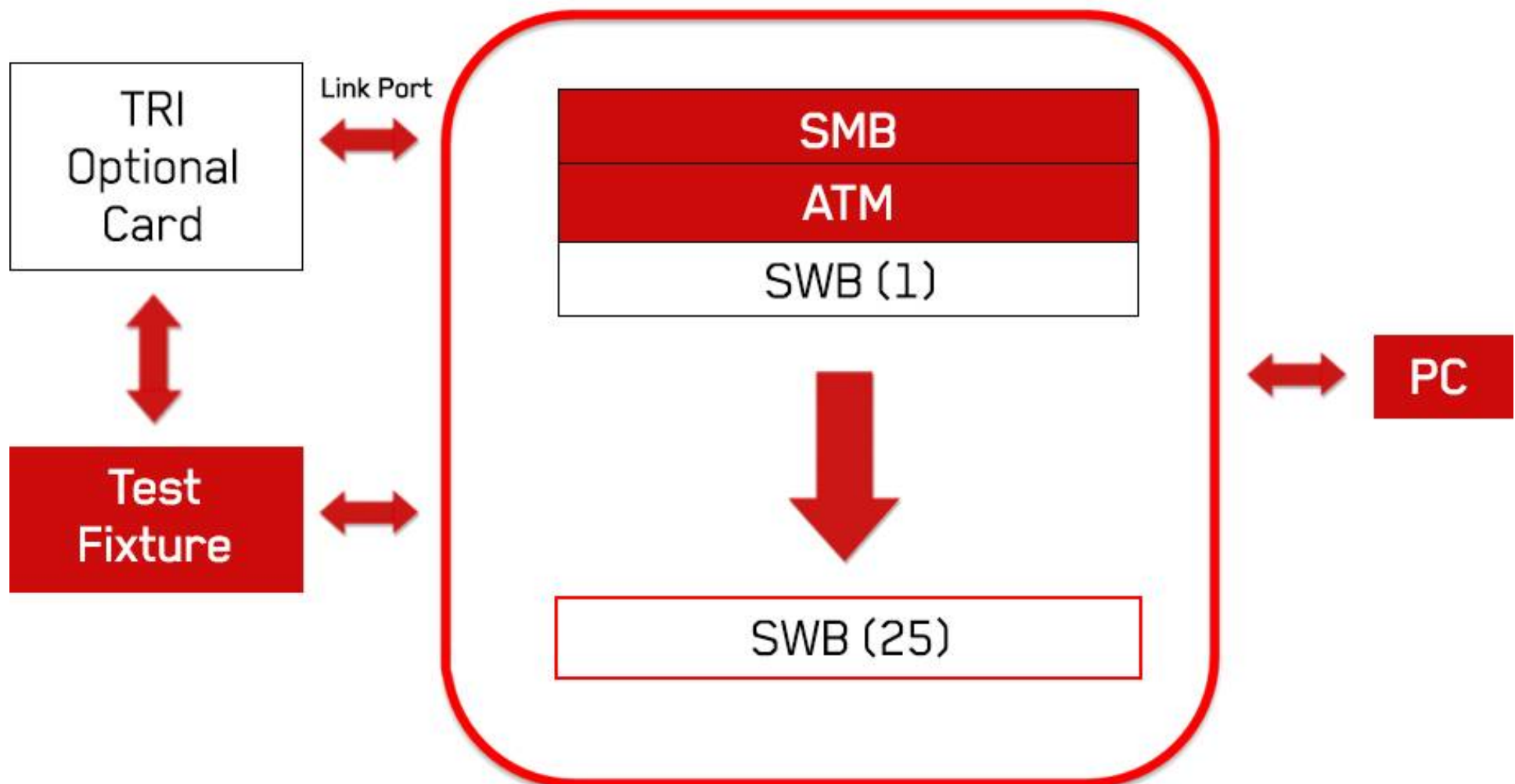
TR5001 SII Parallel ICT Case Study



	TR5001D SII	TR5001Q SII
		
Chasis	EIA-19 INCH RACK MOUNT	LARGE SIZE
Cores	Dual Core	Quad Core
SWB Slots Per Core	13	8
Max Pins Per Core	1664	1024
MAx Pins Per Rack	3328	4096
Support Flexible Merge-Core	3328 Pin (1664 Pin x 2 Core) 3328 Pin (3328 Pin x 1 Core)	4096 Pin (1024 Pin x 4 Core) 4096 Pin (2048 Pin x 2 Core) 4096 Pin (4096 Pin x 1 Core)

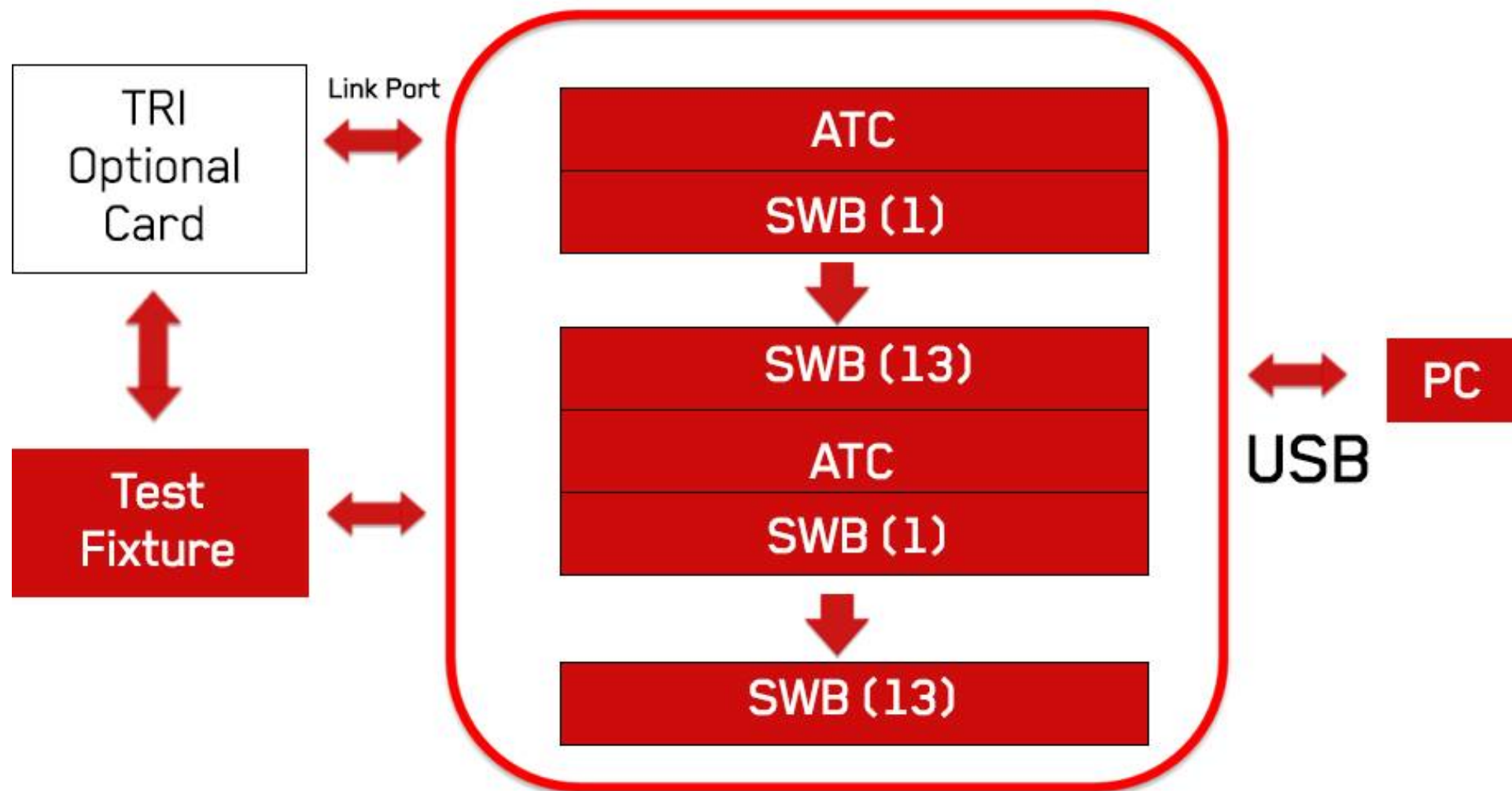
Support Analog & Digital Cards at the Same Tester

TR5001





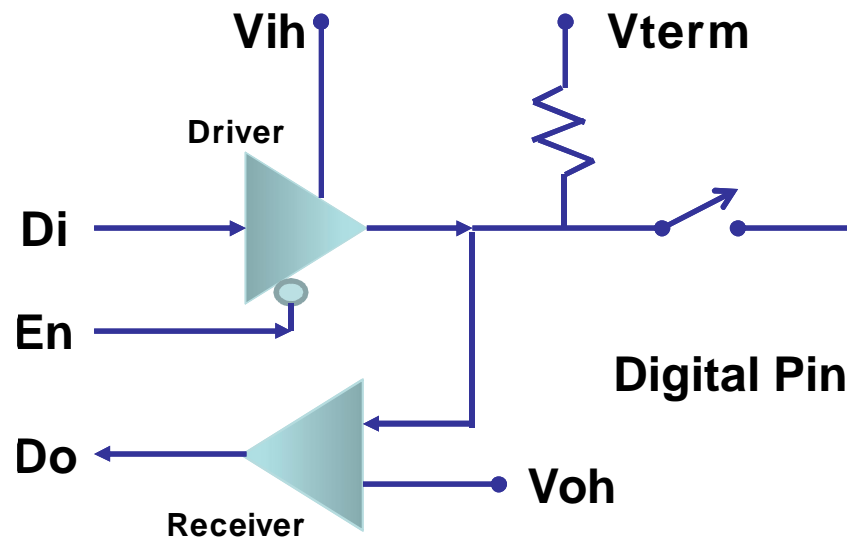
TR5001 SII





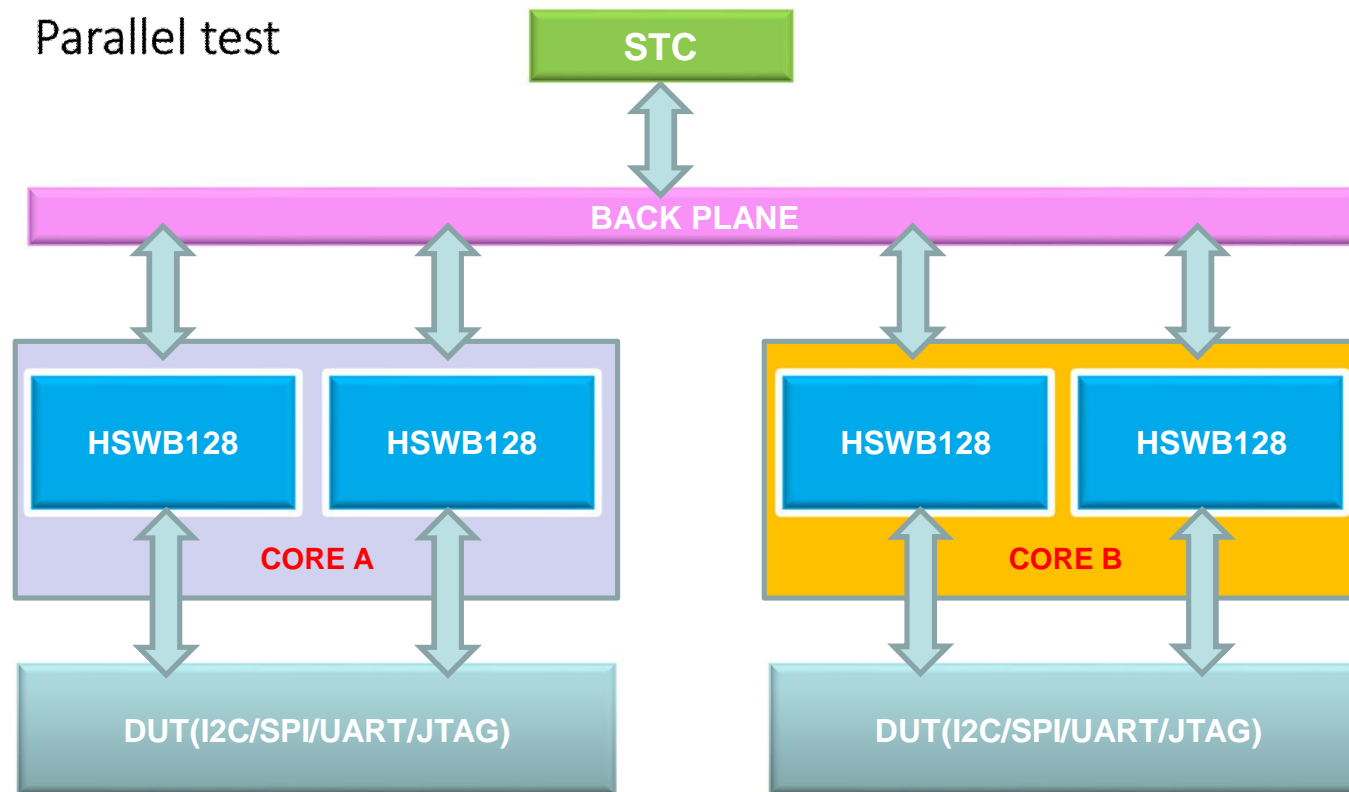
New Hybrid 128Pin SWB :

- 128 analog and digital pins
- 1:1 per pin driver and receiver with per pin level setting
- Up to 200mA driver current
- V_{ih} : 0.5V~4.2V with 20 mV programmable step
- V_{term} : 0V~4.2V with 20 mV programmable step
- V_{oh} : 0V~5V with 20 mV programmable step
- Supports STC function





- STC (Serial Test Controller) provides two channels of high speed JTAG/SPI/UART/I2C bus for any pin in each core.
- Each core has 2 TAPs.
- Up to 2MHz clock rate
- Parallel test





DUT PWR BOARD

- 3.3V/5V/12V/24V 3A Output Rating
- 2 Channels of Programmable Power with 0.2V~24V 3A Output Rating
- OVP and OCP for output protection

APPS (Accurate Programmable Power Source)

- High accuracy, and high power rating
- 0V~75V/8A(200W Maximum) output
- OVP, OCP, OTP for output protection
- USB interface
- Compact size





- **Supported TR5001 Functional Test Modules include:**
 - MFB: Battery emulator and programmable power supply for Phone/Tablet
 - OBP: On board programming for EEPROM/Flash/FirmwareHUB
 - DLB: DC Active load board
 - VRB,HVB: Air condition product test
 - AC Power Source
- **Supported TR5001 USB Devices include:**
 - APPS 7508
 - BScan2: Two channels of JTAG/SPI/I2C/UART programming/test
- **Supported TR5001 Link Port Devices include:**
 - Single-end / Diff-end Counter card : 100MHz/200MHz frequency counter card
 - LED Color Sensor card : LED Color/Hue/Luminance test
 - ADM Card: Analog and Digital MIC test

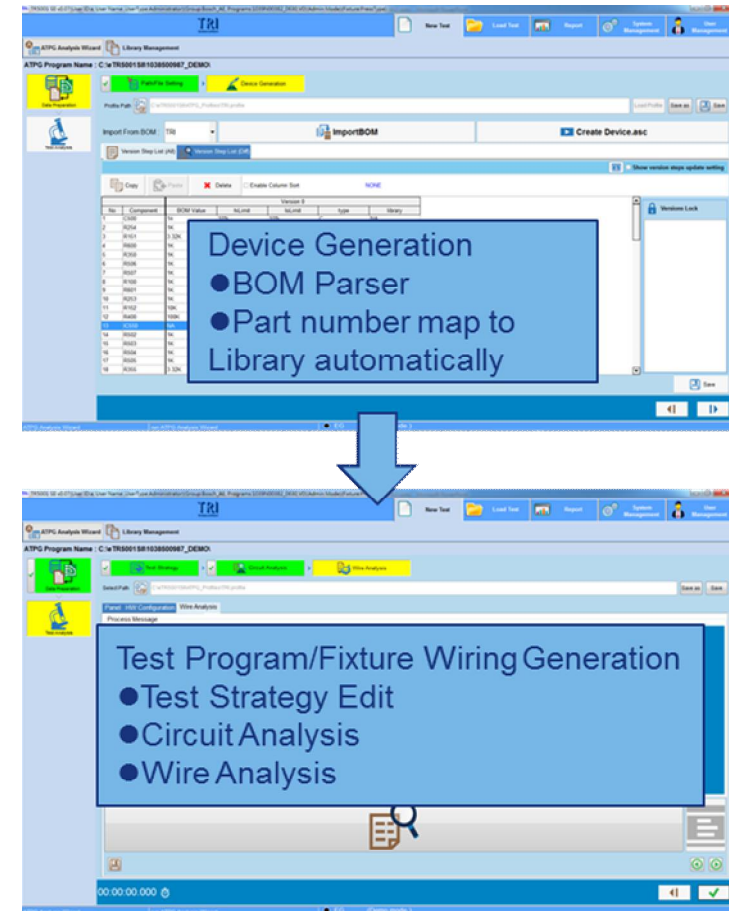


- Auto-sensing safety
- Prevents operator injury
- Hands-free test start
 - No button pressing required
- Industry approved solution
 - Complies with strictest safety requirements

- Brand new software UI:
 - Easy operation/programming
 - Simplify setting items/flow (with Template)
 - Auto-tune tool
- Open platform (Slave mode) : provide DLL functional call
- Generate Test program based on CAD data (with Vayo Test Expert)
 - ODB++, Allegro, PADS, UNIDAT, over 20 supported types
- Test program converter (MDA)
 - TERADYNE Z18/TS12x
 - KEYSIGHT i1000/i3070
 - Digital Test
 - CHECKSUM
- Supported OS: Win7 64bit, Win10 64bit
- YMS 4.0, YMS Lite support



- Brand-New ATPG
- From CAD/BOM to Test Program Generation
- More Integrated & Clearer ATPG Flow
- More Automated
 - Auto Library
 - Auto Guarding Pin Selection
- Higher Coverage Test Program
 - Analog Group Test
 - Drive-Through Test
 - Boundary-Scan Interconnect Test
- Fixture Wiring Data for Panel Board





- Flow-Based Program Debug
- Easy to follow debugging steps

TRIS001T SltUser ID:a, User Name:UserType:Administrato(Group:ICTBox_2, Projects:5001T ICTBOX_1024_ALL_HW0_2st)

TRI

System Config Program Debug Test Plan Production Test

File Edit Program Test View

Board View Pin Search Single Step Single Step Measure Analysis Page Range All Repeat Future Setup Auto Close Test Message Dialog

Step	Part Name	Comment	BOM V	Expect V	Offset	HDev%	LDev%	Mode	Type	V	HR	Lst	Delay	S1	S2	S3	S4	S5	Skp	Meas-V	Dev%
1	RSHOR747		20.78a	20.78a	0.00	100	100	2	R	0	31	161	0	0	0	0	0	0	0	20.56a	-1.0
2	RSHOR746		10.86a	10.86a	0.00	100	100	2	R	0	31	33	0	0	0	0	0	0	0	20.55a	-0.7
3	RSHOR744		20.71a	20.71a	0.00	100	100	2	R	0	31	33	0	0	0	0	0	0	0	0.00000a	0.0
4	RSHOR743		10.78a	10.78a	0.00	100	100	2	R	0	31	32	0	0	0	0	0	0	0	0.00000a	0.0
5	RSHOR742		15.02a	15.02a	0.00	100	100	2	R	0	27	158	0	0	0	0	0	0	0	0.00000a	0.0
6	RSHOR738		16.77a	16.77a	0.00	100	100	2	R	0	27	30	0	0	0	0	0	0	0	16.63a	-0.7
7	RSHOR735		20.75a	20.75a	0.00	100	100	2	R	0	22	154	0	0	0	0	0	0	0	20.57a	-0.3
8	RSHOR734		15.81a	15.81a	0.00	100	100	2	R	0	22	153	0	0	0	0	0	0	0	15.63a	-1.1
9	RSHOR733		10.85a	10.85a	0.00	100	1.0	2	R	0	22	152	0	0	0	0	0	0	0	10.69a	-1.4
10	RSHOR730		20.71a	20.71a	0.00	100	100	2	R	0	22	26	0	0	0	0	0	0	0	20.58a	-0.6
11	RSHOR729		15.72a	15.72a	0.00	100	100	2	R	0	22	25	0	0	0	0	0	0	0	15.54a	-0.5
12	RSHOR728		10.78a	10.78a	0.00	100	100	2	R	0	22	24	0	0	0	0	0	0	0	10.70a	-0.6
13	RSHOR725		24.76a	24.76a	0.00	100	100	2	R	0	1	149	0	0	0	0	0	0	0	24.57a	-0.3
14	RSHOR725		19.82a	19.82a	0.00	100	100	2	R	0	1	148	0	0	0	0	0	0	0	19.74a	-0.3
15	RSHOR724		14.84a	14.84a	0.00	100	100	2	R	0	1	147	0	0	0	0	0	0	0	14.78a	0.3
16	RSHOR723		11.56a	11.56a	0.00	100	100	2	R	0	1	146	0	0	0	0	0	0	0	11.51a	-0.3
17	RSHOR722		8.224a	8.224a	0.00	100	100	2	R	0	1	145	10	0	0	0	0	0	0	8.309a	1.0
18	RSHOR715		24.71a	24.71a	0.00	100	100	2	R	0	1	21	10	0	0	0	0	0	0	24.65a	-0.2
19	RSHOR74		14.75a	14.75a	0.00	100	100	2	R	0	1	19	0	0	0	0	0	0	0	14.79a	0.2
20	R-0.005-7		0.0390a	0.0390a	0.00	0.090a	0.020a	7	R	0	1	2	0	129	130	0	0	0	0	0.0339a	-2.9
21	R-n m5a		0.0390a	0.0390a	0.00	0.090a	0.020a	8	R	0	1	2	0	129	131	0	0	0	0	0.0306a	-1.4

Total Test Time:0.000ms
Total Fail Steps:1
Total Test Time:90.384ms

Program:TRIS001T SltUser ID:a, User Name:UserType:Administrato(Group:ICTBox_2, Projects:5001T ICTBOX_1024_ALL_HW0_2st)





- Show panel board testing result in debug mode

The screenshot displays the EQUIPTEST software interface in debug mode. The window title is "TR50017 SR(User ID:TRI, User Name:Administrator(Group:Multi board, Projects:Debug-test))". The interface includes a menu bar (File, Edit, Program, Text, View) and a toolbar with icons for Board View, Pin Search, Single Step, Repeat Test, and Abort. A left sidebar contains various test control buttons like Failure, Overheat, I-Clearing Done, E-Clear, Component, On Power, Panel Board, and OK. The main area shows a table of test results for two boards, Board 1 and Board 2, each with two cores, Core A and Core B. The table columns include Step, Part Name, and various test parameters like HPin, LoPin, StimV, ADC Value, Meas, and Test Time. A dialog box is open over the table, showing details for a specific test step.

Step	Part Name	HPin	LoPin	StimV	ADC Value	Meas	Test Time	Q3	Q4	Q5	Stk	Meas-V	Dr%
78	R190K_5	46	47	0.200000	7915.1569	198.29217	30.359ms	0	0	0	179.00K	-0.7	
79	R190K_1	47	46	0.200000	7915.1305	198.27320	30.632ms	0	0	0	178.62K	-0.7	
80	R190K_2	47	46	0.200000	7915.1305	198.27320	30.632ms	0	0	0	178.62K	-0.7	
81	R200K_3	47	46	0.200000	7915.1305	198.27320	30.632ms	0	0	0	178.62K	-0.7	
82	R200K_4	47	46	0.200000	7915.1305	198.27320	30.632ms	0	0	0	178.62K	-0.7	
83	R200K_5	47	46	0.200000	7915.1305	198.27320	30.632ms	0	0	0	178.62K	-0.7	
84	R200K_6	47	46	0.200000	7915.1305	198.27320	30.632ms	0	0	0	178.62K	-0.7	
85	R200K_1	47	46	0.200000	7915.1305	198.27320	30.632ms	0	0	0	178.62K	-0.7	
86	R200K_2	47	46	0.200000	7915.1305	198.27320	30.632ms	0	0	0	178.62K	-0.7	
87	R11M_3	16	18	0.200000	7645.8130	100.000	20.590ms	0	0	0	1.003M	0.3	
88	R50_1K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
89	R50_2K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
90	R50_3K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
91	R50_4K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
92	R50_5K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
93	R50_6K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
94	R100_1K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
95	R100_2K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
96	R100_3K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
97	R100_4K2	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	
98	DATA_KAT	68	70	0.200000	7652.380000	102.036154	3.230ms	0	0	0	179.59K	-0.7	



- Debug Multiple Programs on Multiple Cores simultaneously

The screenshot displays the EQUIPTEST software interface. At the top, two red boxes labeled 'Program 1' and 'Program 2' are positioned above two other red boxes labeled 'Core A' and 'Core B'. The software window shows a 'Program Debug' tab with a dialog box asking 'Are you sure to change to the program(Debug test)?'. Below the dialog, a table of test results is visible, with columns for Step, Part Name, Comment, BCM V, Expect V, Offset, H-Limits, L-Limits, Mode, Type, V, H/L, L/H, Delay, O1, O2, O3, O4, O5, O6, O7, O8, O9, O10, O11, O12, O13, O14, O15, O16, O17, O18, O19, O20, O21, O22, O23, O24, O25, O26, O27, O28, O29, O30, O31, O32, O33, O34, O35, O36, O37, O38, O39, O40, O41, O42, O43, O44, O45, O46, O47, O48, O49, O50, O51, O52, O53, O54, O55, O56, O57, O58, O59, O60, O61, O62, O63, O64, O65, O66, O67, O68, O69, O70, O71, O72, O73, O74, O75, O76, O77, O78, O79, O80, O81, O82, O83, O84, O85, O86, O87, O88, O89, O90, O91, O92, O93, O94, O95, O96, O97, O98, O99, O100, O101, O102, O103, O104, O105, O106, O107, O108, O109, O110, O111, O112, O113, O114, O115, O116, O117, O118, O119, O120, O121, O122, O123, O124, O125, O126, O127, O128, O129, O130, O131, O132, O133, O134, O135, O136, O137, O138, O139, O140, O141, O142, O143, O144, O145, O146, O147, O148, O149, O150, O151, O152, O153, O154, O155, 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O820, O821, O822, O823, O824, O825, O826, O827, O828, O829, O830, O831, O832, O833, O834, O835, O836, O837, O838, O839, O840, O841, O842, O843, O844, O845, O846, O847, O848, O849, O850, O851, O852, O853, O854, O855, O856, O857, O858, O859, O860, O861, O862, O863, O864, O865, O866, O867, O868, O869, O870, O871, O872, O873, O874, O875, O876, O877, O878, O879, O880, O881, O882, O883, O884, O885, O886, O887, O888, O889, O890, O891, O892, O893, O894, O895, O896, O897, O898, O899, O900, O901, O902, O903, O904, O905, O906, O907, O908, O909, O910, O911, O912, O913, O914, O915, O916, O917, O918, O919, O920, O921, O922, O923, O924, O925, O926, O927, O928, O929, O930, O931, O932, O933, O934, O935, O936, O937, O938, O939, O940, O941, O942, O943, O944, O945, O946, O947, O948, O949, O950, O951, O952, O953, O954, O955, O956, O957, O958, O959, O960, O961, O962, O963, O964, O965, O966, O967, O968, O969, O970, O971, O972, O973, O974, O975, O976, O977, O978, O979, O980, O981, O982, O983, O984, O985, O986, O987, O988, O989, O990, O991, O992, O993, O994, O995, O996, O997, O998, O999, O1000.

One click to change the debugged program



- Customize your own test flow

- Test Items Edit
- Test Flow Edit (from top to bottom run)

The screenshot displays the EQUIPTEST software interface. At the top, there is a menu bar with options like 'New Test', 'Load Test', and 'Management'. Below this is a toolbar with icons for 'Select Programs', 'System Config', 'Program Debug', 'Test Plan', and 'Production Test'. The main window shows a 'Test Program' section with a table of fixtures and test items. A 'New Test Item' dialog is open, showing a list of test actions such as 'Barcode Actions', 'Fixture Control Actions', and 'ICT Test Actions'. A red arrow points from the 'New Test Item' dialog to a 'Test Flow Conditions' dialog. This dialog has sections for 'Pre-Test Setting' and 'Post-Test Setting', each with radio buttons for 'NONE', 'PASS', and 'FAIL', and a 'Retry' field set to '2'. A 'System Message' box is visible at the bottom of the interface.

**YOUR
TEST A
INSPEC
PARTN**

Test Flow Conditions

Pre-Test Setting	
Pre-Test Item Result	<input checked="" type="radio"/> NONE <input type="radio"/> PASS <input type="radio"/> FAIL
Post-Test Setting	
Abort Test	<input checked="" type="radio"/> NONE <input type="radio"/> PASS <input type="radio"/> FAIL
Retry	2

TRI Provides a One
PCB Assembly Test

System Message



- Charts for Intuitive Production Status: Yield Rate Distribution, Top Defects, ...



- ATM Auto Calibration

TRI: TR5001 SZ v0.07 (User ID: User Name, User Type Administrator, Group Box/AE, Programs: 1019H00182_0630 V0) (Admin Mode) (Fixture Press Type)

TRI

New Test Load Test Report System Management User Management

System Parameters System Check Calibration Interface Language

Core_A Core_B Core_C Core_D

ATM

System Ready

Elapsed Time: 00:00:00

Flow Status

Warm Up Cable Check System Self Check Diagnostic 1 Calibration Diagnostic 2 System Self Check End

USB RS-232

EXT DMM

DMM 11.5 VDC

Calibration Calibration ● EG (Demo mode)

- Multi/Merge Core Parallel Testing
- Improved Test Accuracy and Capability
- New and Improved Test Modules
- Intelligent Software Interface
- Smart Safety Solutions
- Compatibility with TR5001 Series
- POGO PIN QDI Fixture Interface
- Inline and Manual Handler



Q&A

QUESTIONS & ANSWERS SESSION



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Thank You